

**Amendments to the Abstract:**

Please substitute the following paragraph for the abstract now appearing in the currently-filed specification:

The invention relates to a process for designing semi-conductor memory components, especially DRAM components, whereby the process comprises the following steps: designing a first layout for a module of the semi-conductor memory component to be used in a first configuration of the semi-conductor memory component; designing a second layout for the semi-conductor memory component module to be used in a second configuration of the semi-conductor memory component; and using the first layout or the second layout for the total layout of the semi-conductor memory component, depending on the current configuration of the semi-conductor memory component.